**IEEE P802.15**

**Wireless Personal Area Networks**

|  |  |
| --- | --- |
| Project | IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs) |
| Title | **SNUST – VTASC PHY PPDU Format and PIB Attributes Specification Revision** |
| Date Submitted | January, 2017 |
| Source | Jaesang Cha, Kim Chan (SNUST), Byungjun Min (Head IT Co.,Ltd.), Jaekwon Shin, Jintae Kim (Fivetek Co., Ltd), Daehyun Kim, Dongwoo Lee (Namuga Co., Ltd), Juphil Cho (Kunsan Nat’ Univ.), Hyunsuk Hwang (Seoil Univ.), Yunsik Lim (Yeoju Institute of Technology), Gilsik Lee (Univ. of Texas), Ilkyoo Lee (Kongju Nat’ Univ.), Sooyoung, Chang (CSUS), Vinayagam Mariappan (SNUST) | Voice: [ ]Fax: [ ]E-mail: [chajs@seoultech.ac.kr] |
| Re: | Draft D1 Comment Resolution based VTASC PHY PPDU Format and PIB Attributes Specification Revision |
| Abstract | Details of Resolutions regarding to the submitted Comments on D1 are suggested for VTASC PHY PPDU Format and PHY PIB Attributes Specification Revision. The VTASC method is designed to operate on the application services like LED ID, Digital Signage with Advertisement Information etc. |
| Purpose | D1 Comments Resolutions and Editorial Revision. |
| Notice | This document has been prepared to assist the IEEE P802.15. It is offered as a basis for discussion and is not binding on the contributing individual(s) or organization(s). The material in this document is subject to change in form and content after further study. The contributor(s) reserve(s) the right to add, amend or withdraw material contained herein. |
| Release | The contributor acknowledges and accepts that this contribution becomes the property of IEEE and may be made publicly available by P802.15. |

# **1. PPDU FORMART FOR VTASC**

# **VTASC PPDU Format**

The PPDU frame structure Display Light Pattern Based Transmitter with VTASC is formatted as illustrated in Figure 4-1 for PHY-VI 2-Dimensional codes

****

**Figure 4-1 – VTASC PPDU Format**

**8.6.8.5.1 SHR Field**

The SHR field is used by the transceiver to obtain optical clock synchronization with an incoming message is called Preamble. The standard defines one fast locking pattern (FLP) followed by choice of four topology dependent patterns (TDPs) for the purposes of distinguishing different PHY topologies is shown in Table 4-1.



**Table 4-1 – Preamble Pattern with Topologies**

**8.6.8.5.2 PHR Field**

The PHY VI header is described as shown in Table 4-2 and shall be transmitted with data to identify the PHY Mode, Data rate, and PSDU length etc. to identify the transmission specification.

****

**Table 4-2 – PHY Header**

Burst Mode Field: The burst mode bit indicates that the next frame following the current frame is part of the burst mode. The Burst Mode bit shall be set TRUE if the burst mode is being used otherwise, the Burst Mode bit shall be set FALSE.

Channel Number Field: The channel number field for PHY shall be the band plan ID of the lowest wavelength. Refer to 9.3.1 for more detailed information.

MCS ID Field: The modulation and coding scheme (MCS) ID shall be indicated in the PHY header based on Table 83.

PSDU Field: The PSDU length field specifies the total number of octets contained in the PSDU.

**8.6.8.5.3 PSDU Field**

The PSDU field has a variable length and carries the data of the PHY VI frame. The FCS is appended if the PSDU has a non-zero byte payload. The structure of the PSDU field is as shown in Figure 4-2.



**Figure 4-2 – VTASC PHY PSDU Field Structure**

# **2. PHY PIP ATTRIBUTES FOR VTASC**

# **PHY PIB Attributes**

The PHY PIB comprises the attributes required to manage the PHY sublayer of a device. The attributes contained in the IEEE802.15.7-2011 PHY PIB are presented in Table 125 - PHY PIB Attributes.

The additional PHY PIB attributes added on PHY for VTASC is presented in the Table 128 — PHY PIB attributes (continued).

|  |
| --- |
| **PHY PIB Table 188 Additions** |
| **Attribute** | **Identifier** | **Type** | **Range** | **Description** |
| phyVTASCApplicationSpecificMode | 0x10 | Unsigned | 0~255 | This attribute specifies the application specific PHY mode.0 : Normal Data (Media Content, Information Content based on the Application used for)1 : LED ID Data 2 : Authentication Data |
| phyVTASCFreq | 0x11 | Integer | 0-65535 | This attribute specify the frame rate of VTASC sequence Transmission |
| phyVTASCTLevel | 0x12 | Integer | 0-65535 | This attribute specify the transparency Level of the VTASC |
| phyVTASCAHSize | 0x13 | Integer | 0-65535 | This attribute specify the no of Vertical Blocks in the VTASC |
| phyVTASCAVSize | 0x14 | Integer | 0-65535 | This attribute specify the no of Horizontal Blocks in the VTASC |
| phyVTASCSModel | 0x15 | Integer | 0-65535 | This attribute specify the Block Shape Type used in the VTASC0 : Square1 : Rectangle2 : Circle3 : Triangle4 : Ellipse 5 : Star6~65535 : Reserved |
| phyVTASCCValue | 0x16 | Integer | 0-65535 | This attribute specify the no of Colors used in the VTASC |

 **Table 188 — VTASC PHY PIB attributes (continued for VTASC)**