**IEEE P802.15**

**Wireless Personal Area Networks**

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| Project | IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs) | |
| Title | **SNUST – VTASC PHY Layer Operating Modes and Specifications Revision** | |
| Date Submitted | January, 2017 | |
| Source | Jaesang Cha, Kim Chan (SNUST), Byungjun Min (Head IT Co.,Ltd.), Jaekwon Shin , Jintae Kim (Fivetek Co., Ltd), Daehyun Kim , Dongwoo Lee (Namuga Co., Ltd), Juphil Cho (Kunsan Nat’ Univ.), Hyunsuk Hwang (Seoil Univ.), Yunsik Lim (Yeoju Institute of Technology), Gilsik Lee (Univ. of Texas), Ilkyoo Lee (Kongju Nat’ Univ.), Sooyoung Chang (CSUS), Vinayagam Mariappan (SNUST) | Voice: [ ] Fax: [ ] E-mail: [chajs@seoultech.ac.kr] |
| Re: | Draft D1 Comment Resolution based VTASC PHY Layer Operating Modes and Specifications Revision | |
| Abstract | Details of Resolutions regarding to the submitted Comments on D1 are suggested for VTASC PHY Layer Operating Modes and PHY Specifications. The VTASC method is designed to operate on the application services like LED ID, Digital Signage with Advertisement Information etc. | |
| Purpose | D1 Comments Resolutions and Editorial Revision. | |
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# **1. PHY LAYER OPERATING MODES FOR VTASC**

# **Operating Modes**

The display light pattern based Variable Transparent Amplitude-Shape-Color modulation transmitter uses the PHY VI – 2 Dimensional / Screen Source.

The PHY VI Operating Modes system specifications are given in Table 81. The additional PHY Operating Modes supported by VTASC is presented the Table 81 – PHY IV Operating Modes (continued).

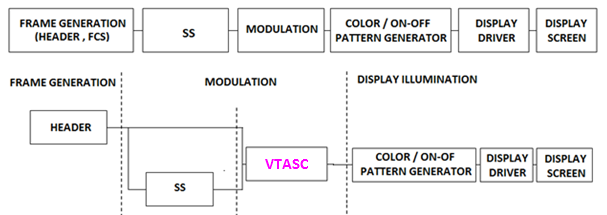
|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **PHY Operating Modes** | | | | |
| **Modulation** | **RLL Code** | **Optical Clock Rate** | **FEC** | **Data Rate (Kbps)** |
| VTASC | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 512 Kbps |
| SS VTASC | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 256 Kbps |

**Table 120 – PHY Operating Modes (continued for VTASC)**

# **2. PHY SPECIFICATIONS FOR VTASC**

# **VTASC**

The VTASC PHY supported data rates and operating conditions are shown in Table 81 – PHY VI Operating Modes. The Display Light Pattern Based VTASC Transmitter works with variable size and different shape of the patterns. The data embedded on visual frame by overlaying visual patterns displays visual area. The PHY system diagram illustrated in Figure 2-1 is 2 Dimensional / Screen Source based Transmitter using VTASC.

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**Figure 2-1 –Display Light Pattern Based Transmitter with VTASC** **PHY System Diagram**

The PHY for VTASC designed with specific key features in consideration to have error free and effective display to camera communication in the real-time usage of end system. The design goals are,

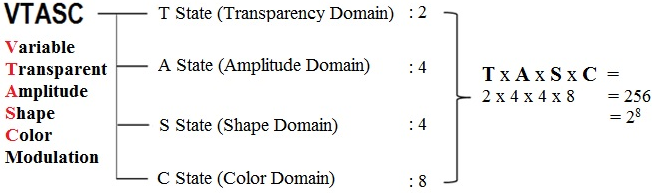
* Angle and Distance Free Communication
* Rx Distance Adaptive Communication by Screen with interactive Camera
* Asynchronous Communication
* Rx Frame Rate independent Transmission
* Multi-Display Model for Transmission

To achieve described design goal, the PHY design is proposed with Spread Spectrum based VTASC. The use cases of the modulation scheme and SS Modulation parameter are described in this section.

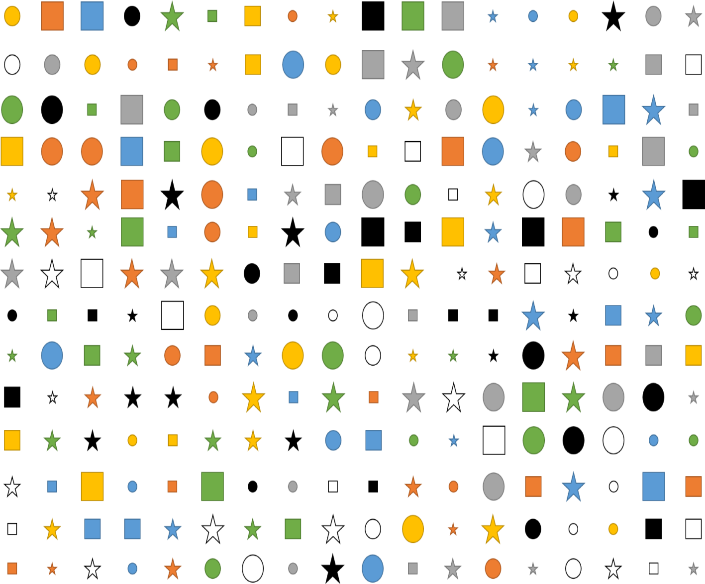
**15.2.1 VTASC Modulation**

Color Shift Keying (CSK) is one of the promising modulation formats specifically for Display based VLC system. In order to improve the distance and angle free with higher bitrate, the new proposed color based modulation scheme called Variable Transparent Amplitude-Shape-Color (VTASC) Modulation is proposed. VTASC is one of the promising modulation formats specifically for display based VLC system with improved VLC throughput by increasing the bit per symbol rate, and avoiding the single color interference.

The VTASC is coded by T (Transparency) / A (Amplitude) / S (Shape) / C (Color) State as described in the below;



The no of coded Levels in the VTASC schemes (TxAxSxC): 256 = 28 and this makes place to code 8 bit Symbol with 2/4/4/8 (Transparency/Amplitude/Shape/Color). The coded sample model is given Figure 2-2.



**Figure 2-2 – VTASC Coded Pattern Model**

The following Table 2-1 describes the Symbol bit mapping using VTASC schemes. The VTASC is able to expand a Domain size and add a transparency or blinking domain.

|  |  |  |
| --- | --- | --- |
| **VTASC Block Model**  **(TxAxSxC)** | **No of Block Types (T\*A\*S\*C)** | **BitsPerSymbol** |
| T = 2, A = 2,S = 2,C = 2 | 16 = 24 | 4 |
| T = 2, A = 4,S = 2, C = 2 | 32 = 25 | 5 |
| T = 2, A = 4,S = 4, C = 2 | 64 = 26 | 6 |
| T = 2, A = 8,S = 4, C = 2 | 128 = 27 | 7 |
| T = 2, A = 2,S = 4, C = 4 | 64 = 26 | 6 |
| T = 2, A = 4,S = 4, C = 4 | 128 = 27 | 7 |
| T = 2, A = 8,S = 4, C = 4 | 256 = 28 | 8 |
| T = 2, A = 4,S = 2, C = 8 | 128 = 27 | 7 |
| T = 2, A = 4,S = 4, C = 8 | 256 = 28 | 8 |
| T = 2, A = 8,S = 2, C = 8 | 256 = 28 | 8 |
| T = 2, A = 8,S = 4, C = 8 | 512 = 29 | 9 |
| T = 2, A = 8,S = 4, C = 16 | 1024 = 210 | 10 |

**Table 2-1 – VTASC Symbol per Bit Mapping**

The Data Rate calculated using following Mathematical representation,

DataRate = (NoofBlocks \* BitsPerSymbol \* OpticalClockrate \* FECRate) / CodeLength)

Where, “CodeLength” is 1 for without SS Coded schemes and respective code length for with SS Coded Schemes

Note this case study designed with Full HD Display (1920x1080) with minimum block size of 32x32 pixels and this data rate calculation vary from Display Screen Size.

NoofBlocks = (1920/32)\* (1080/32) = 60 x 32 (Approximated to even multiplication for coding efficiency)

The Data Rate for 2 Color VTASC Code with 8 size scalability & 4 shapes & 2 transparency Level without SS Coded Code (CodeLength is 1),

BitsPerSymbol = 7 (Refer Table 2-1)

OpticalClockrate = 30

FECRate = 1 (Refer Table 1-2)

DataRate = ((1920/32)\* (1080/32) \* 7 \* 30 \* 1) / 1) = 403200 Approximated to 390 Kbps

The Data Rate for 2 Color VTASC Code with 8 size scalability with SS Coded Code (Minimum CodeLength is 2),

BitsPerSymbol = 4 (Refer Table 2-1)

OpticalClockrate = 30

FECRate = 1 (Refer Table 1-2)

DataRate = ((1920/32)\* (1080/32) \* 4 \* 30 \* 1) / 2) = 201600 Approximated to 195 Kbps

The Table 2-2 describes the data rate supported based on predefined block size.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Modulation**  **(TxAxSxC)** | **RLL Code** | **Optical Clock Rate** | **FEC** | **Data Rate (Kbps)** |
| 2 Color VTASC Code1  (T = 2,A=2/4/8,S=2/4,C=2) | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 390 Kbps |
| 4 Color VTASC Code1  (T = 2,A=2/4/8,S=2/4, C=4) | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 450 Kbps |
| 8 Color VTASC Code1  (T = 2,A=2/4/8,S=2/4,C=8) | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 506 Kbps |
| 16 Color VTASC Code2  (T = 2,A=2/4/8,S=2/4,C=16) | None | 30Hz | RS(64,32)/ RS(160,128)/ None | 1054 Kbps |
| 2 Color SS VTASC Code1  (T = 2,A=2/4/8,S=2/4,C=2) | None | 30Hz | None | 195 Kbps |
| 4 Color SS VTASC Code1  (T = 2,A=2/4/8,S=2/4,C=4) | None | 30Hz | None | 225 Kbps |
| 8 Color SS VTASC Code1  (T = 2,A=2/4/8,S=2/4,C=8) | None | 30Hz | None | 253 Kbps |
| 16 Color SS VTASC Code2  (T = 2,A=2/4/8,S=2/4,C=16) | None | 30Hz | None | 527 Kbps |

**Table 2-2 – VTASC PHY Data Rate Table**

Note: [32x32 Block Size] 1 and [24x24 Block Size] 2

Where, “T” represents Level of transparency, “A” represents Number of Block Size Amplitude

“S” represents Number of Shapes, “C” represents Number of Colors

**15.2.2 Spread Spectrum**

The spread spectrum adopted with PHY model design for Display Light Pattern Based Transmitter with VTASC to add built-in adaptation on data recovery in addition to achieve the asynchronous communication with Angle free and distance free communication between transmitter and Receiver. The PHY implementation can use Orthogonal Codes (like Walsh sequences) and Non-Orthogonal Codes (like PN, Gold, and Kasami shift register sequences).

In this PHY model used Gold Sequence based Spreading code for encode data. The Study case of Gold Sequence SS Code Specification is as follows,

* Gold sequence was chosen as a spreading code
* Shifter register length is 5
* Code length is 31 (=25-1)
* 4 family code set was generated via offset 8\*n chips of code set 1
* Code Sets

1. Code set 1: 0000000010010100100111101010110 (zero offset)
2. Code set 2: 1001010010011110101011000000000 (8chip offset)
3. Code set 3: 1001111010101100000000010010100 (16chip offset)
4. Code set 4: 1010110000000001001010010011110 (24chip offset)

The Figure 2-3 shows the SS Gold Sequence Generator model.

5

4

3

2

1

5

4

3

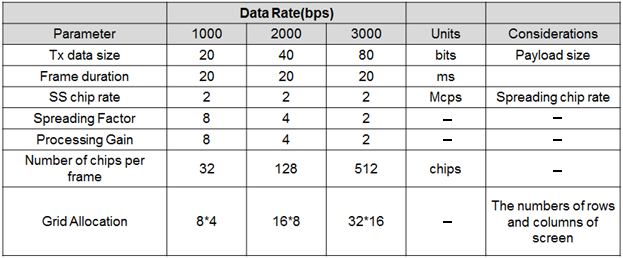
2

1

Gold-Sequence

**Figure 2-3 – Gold Sequence Generator**

The Table 2-3 describes the SS Modulation Parameters adopted for simulating proposed PHY Layer design.



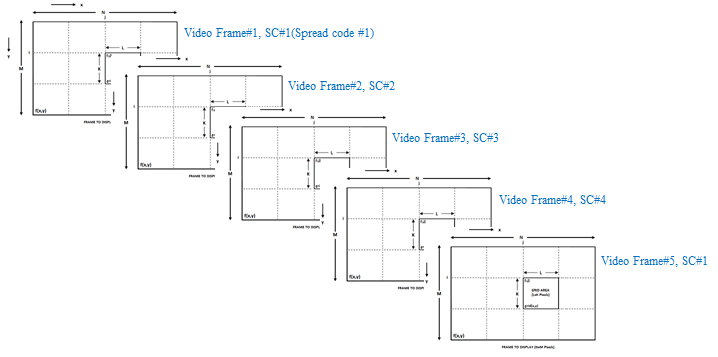
**Table 2-3 – SS Modulation Parameters Study Case**

**15.2.3 Data Encoder**

The Display Light Pattern Based Transmitter with VTASC Schemes works with two data embedding method. The supported data embedding principles are Alpha Blending and Watermarking. The rule to embedding data and data rate achievement vary based on the kind of display used to design the Transmitter.

**15.2.4 Asynchronous Communication Mode**

The PHY for Display Light Pattern Based Transmitter with VTASC designed with Asynchronous communication mode. The Asynchronous communication achieved when transmitting data, different spreading code is used per video frame. Each code sets repeated for spreading data according to spreading factor and each spreading code set 1, 2, 3, and 4 are assigned for successive 4 frames as shown in Figure 2-4.

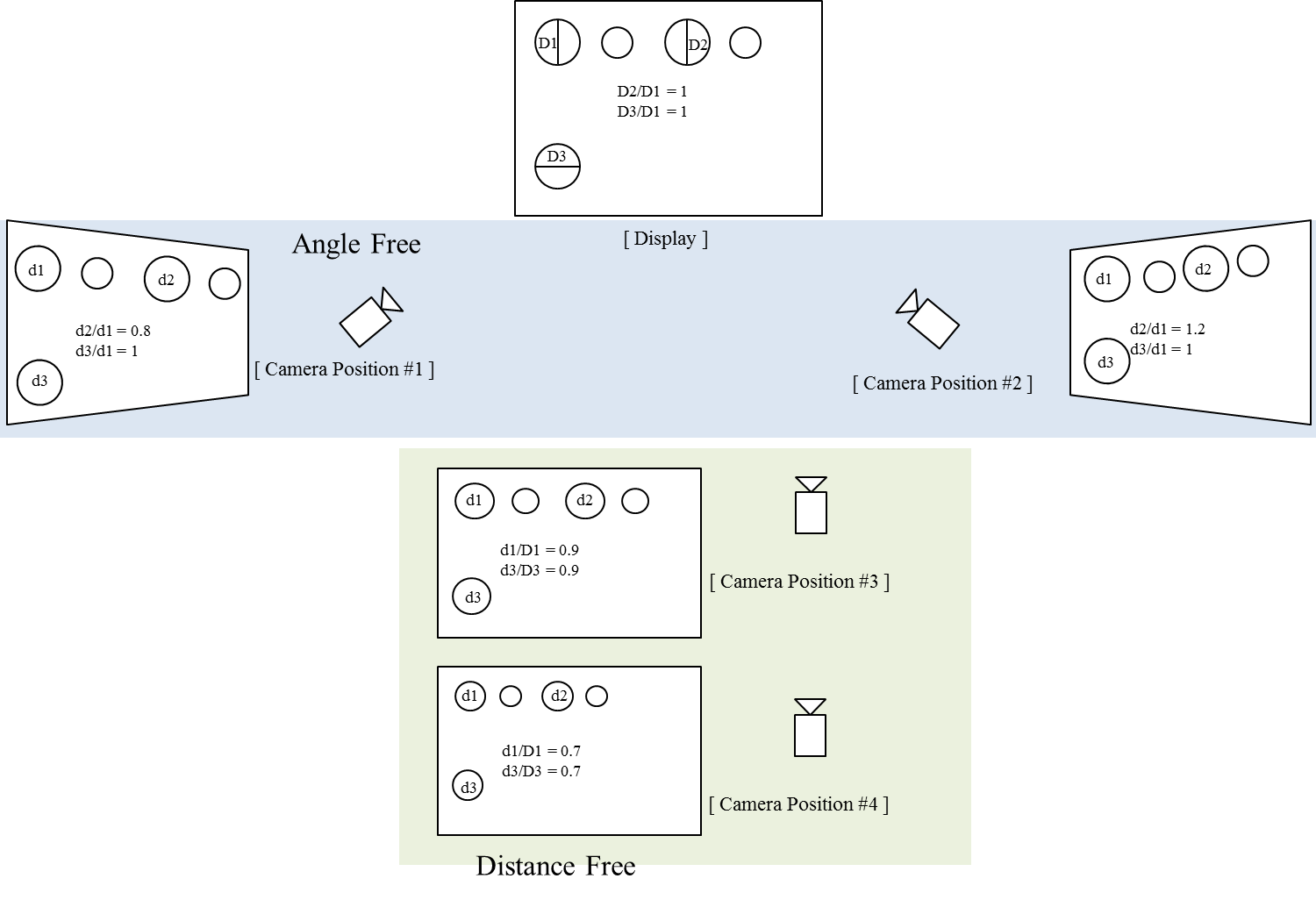


**Figure 2-4 – SS Code Assignment**

The receiver side knows the spreading code of the transmitter synchronize the receiver application automatically. If camera CMOS received same frame, for example #1 video frame receive twice, then receiver will despread video frames using SC#1, SC#2. When processing using SC#2, dominant value will not appear so the video frame will be discarded. The orthogonal spread spectrum sequence is best adopt on PHY design to have easy and fast synchronization.

**15.2.5 Angle Free Communication**

The PHY for Display Light Pattern Based Transmitter with VTASC designed with Angle Free Communication between Transmitter and Receiver is shown in Figure 2-5. The Angle free communication is achieved by Warping the ROI of the transmitter to get the original shape alignment and then the decoded data synchronizing with spread code to extract original information transferred on transmitter. The kind automatic synchronization in receiver is time consuming function but the communication is robust.



**Figure 2-5 – Angle Free and Distance Adaptive**

**15.2.6 Scalable Bitrate Controller**

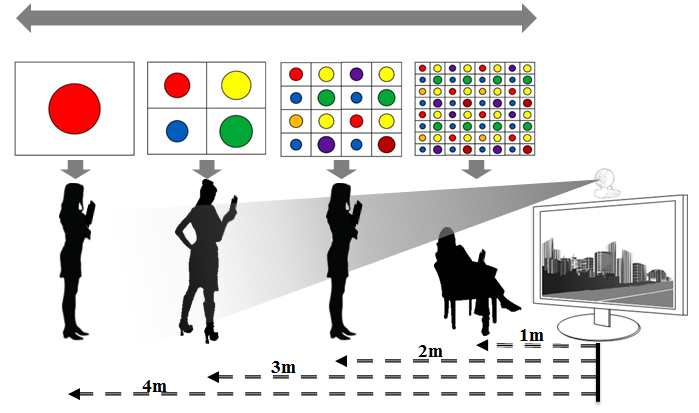
The PHY for Display Light Pattern Based Transmitter with VTASC designed with built-in Scalable bitrate Controller. To achieve robust communication, the scalable data transmission mode is proposed in PHY model design is shown in Figure 2-6. The Screen is divided into Multiple regions and each region has different frame rate controlled data transmission is enabled. This approach adds robustness on system performance for frame rate adaptive communication based on the receiver performance.

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**Figure 2-6 – Scalable Bitrate Controller**

**15.2.7 Distance Adaptive Data Rate Control**

The PHY for VTASC Display TX Schemes designed with distance adaptive data rate control. In this case the Transmitter built-in with camera features as shown in Figure 2-7. The Transmitter Camera Estimate the Receivers distance using camera. There are different methods used to estimate the distance to receiver. Some of these methods are active by sending some signals to the object such as laser range finder, ultrasonic range finder, radio waves, microwaves, infrared, etc. Some others are passive that only receive information about the target position. The distance estimation method decision left up to the system designer.

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**Figure 2-7 – Distance Adaptive Data rate Control**

For this conceptual evaluation, Kinect sensor based triangulation method is used for distance estimation. In this approach, the laser source emits a single beam which is split into multiple beams by a diffraction grating to create a constant pattern of speckles projected onto the scene and this pattern is captured by the infrared camera and is correlated against a reference pattern. The reference pattern is obtained by capturing a plane at a known distance from the sensor, and is stored in the memory of the sensor. When a speckle is projected on an object whose distance to the sensor is smaller or larger than that of the reference plane the position of the speckle in the infrared image will be shifted in the direction of the baseline between the laser projector and the perspective center of the infrared camera. These shifts are measured for all speckles by a simple image correlation procedure, which yields a disparity image. For each pixel the distance to the sensor can then be retrieved from the corresponding disparity.

The sequence code length assignment is based the distance of the receiver from transmitter. If the receiver is near then the SF Value is small so Short Sequence Code is assigned otherwise SF values is high so Long Sequence Code is assigned. In this way, PHY model design control the distance adaptive data rate selection.