**IEEE P802.15**

**Wireless Personal Area Networks**

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| Project | IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs) |
| Title | **draft-D0-text-input-status** |
| Date Submitted | 5 May 2016 |
| Source | [Roberts, Richard D][Intel][address] | Voice: [ ]Fax: [ ]E-mail: [ ] |
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| Abstract | [Description of document contents.] |
| Purpose | [Description of what the author wants P802.15 to do with the information in the document.] |
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The current status of the ISC/LR-PD input for the baseline draft D0 text is shown below. Please provide any corrections.

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| **#** | **ISC/LR-PD Mode** | **Proposer** | **Doc with text detail** | **Text in D0 as of date?** | **Editable Graphics Submitted?** | **Graphics in D0 as of date?** |
| 1 | UFSOOK | Intel | **16/182r0** | **YES** | **YES** | **YES** |
| 2 | Twinkle VPPM | Intel | **16/182r0** | **YES** | **YES** | **YES** |
| 3 | S2-PSK | Kookmin | **16/239r0** | **YES** | **YES** | **NO** |
| 4 | S2+DMS-PSK | Kookmin | **16/239r0** | **YES** | **YES** | **NO** |
| 5 | FSK | Kookmin | **16/240r0** | **YES** | **YES** | **NO** |
| 6 | 2 mode OOK | Kookmin | **16/240r0** | **YES** | **YES** | **NO** |
| 7 | 2D-sequential color code | Kookmin | **16/241r0** | **YES** | **YES** | **NO** |
| 8 | Kookmin Invisible code | Kookmin | **???** | **NO** | **???** | **NO** |
| 9 | 3 mode PWM/PPM | Panasonic | **16/xxxr0** | **NO** | **NO** | **NO** |
| 10 | Offset-VPWM | SNUST | **16/353r2** | **NO** | **YES** | **NO** |
| 11 | VTASC | SNUST | **16/278r0** | **YES** | **YES** | **NO** |
| 12 | 2 code Invisible | SNUST | **16/354r0** | **NO** | **YES** | **NO** |
| 13 | PAPM  | China Telecom | **NO** | **NO** | **NO** | **NO** |
| 14 | CSM | SYCA | **Withdrawn** | **---** | **---** | **---** |
| 15 | RS-FSK | NTU | **16/189r0** | **YES** | **YES** | **NO** |

The current status of the HR-PD input for the baseline draft D0 text is shown below. Please provide any corrections.

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| **#** | **HR-PD Mode** | **Proposer** | **Doc with text detail** | **Text in D0 as of date?** | **Editable Graphics Submitted?** | **Graphics in D0 as of date?** |
| 1 | High-Bandwidth PHY | Fraunhofer | **16/356r0** | **NO** | **NO** | **NO** |
| 2 | Text Proposal | Huawei | **16/360r0** | **NO** | **NO** | **NO** |
| 3 | S2-PSK | PureLiFi | **16/310r0** | **NO** | **YES** | **NO** |

Motion Passed During March 2016 meeting …

**All 15.7r1 proposers (ISC/LR-PD/HR-PD) must post their text input for the baseline draft D0 to mentor two weeks prior to the opening of the May 2016 IEEE802 meeting in order to meet the current 15.7r1 schedule. The committee may assess a severe penalty for missing this deadline.**

**The proposed deadline is 2 May 2016, 23:59 hours US Pacific Daylight Savings Time.**

**The proposed “severe penalty” is that any proposer that misses the deadline will temporarily have their proposal removed from the draft. If they submit their input during the May meeting then they can request the committee reinstate their proposal in the draft text. If they have not submitted by the end of the May meeting then they’re permanently deleted from the draft D0 document.**