IEEE P802.11
Wireless LANs

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| DMG TRN field ambiguity  |
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Abstract

This document presents resolutions to some ambiguities in the use of the DMG TRN only capability and the “DMG TRN” EMDG-A header fielg

**Discussion**

During the discussion of the PICS document it because obvious that there is some ambituity as to the requirement from a STA that has set the DMG TRN Only Capable subfield to 1.

***TGay Editor: Modify the text in P86L22-24 of D1.2 as follows:***

The DMG TRN RX Only Capable subfield is set to one to indicate that the STA is capable of receiving only DMG TRNs as defined in 20.10.2.2.2, even when such TRNs are appended to an EDMG PPDU (see 30.9.2.2.3). Otherwise, this subfield is set to zero.

***TGay Editor: Modify the text in the last paragraph of 10.39.6.4.1 as follows:***

A 2.16 GHz EDMG PPDU transmission that includes the TRN field and is addressed to a STA that has the DMG TRN RX Only Capable subfield set to one in the STA’s EDMG Capabilities element shall have the DMG\_TRN parameter of the TXVECTOR set to one and the EDMG\_TRN\_LEN parameter of the TXVECTOR set to a value greater than 0 and less than 32. Otherwise, the DMG\_TRN parameter of the TXVECTOR shall be set to zero. If the EDMG\_TRN\_LEN parameter of the TXVECTOR of a PPDU sent to a STA that has set the DMG TRN Only Capable subfield to one, is greater than 1, the CH\_BANDWIDTH shall be set to CBW216.

***TGay Editor: Modify the text in the second paragraph of 30.9.2.2.3 as follows:***

An EDMG STA that has the DMG TRN RX Only Capable field in its EDMG Capabilities element equal to zero shall support the following transmit and receive configurations of the EDMG TRN-Unit P, EDMG TRN-Unit M and EDMG TRN-Unit N fields in a PPDU:

***TGay Editor: Modify the text in the last paragraph of 30.9.2.23 as follows:***

When the DMG TRN field is equal to one in a EDMG PPDU that has the EDMG TRN Length field greater than 0, the TRN field appended to the PPDU has the structure of a DMG TRN field defined in 20.10.2.2.2. In this case, the value of the EDMG TRN Length field is smaller than 32 and only one bit is set to one in the BW field.

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**References:**