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Wireless LANs

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| 30.5.6.4.4 Block Interleaver for SC Mode | | | | |
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| Author(s): | | | | |
| Name | Affiliation | Address | Phone | email |
| Yaroslav Gagiev | Intel | Turgeneva 30, Nizhny Novgorod 603024, Russia | +7 (831) 2969444 | yaroslav.p.gagiev@intel.com |
| Artyom Lomayev | Intel |  |  | artyom.lomayev@intel.com |
| Alexander Kostin | Intel |  |  | alexander.kostin@intel.com |
| Miki Genossar | Intel |  |  | miki.genossar@intel.com |
| Alexander Maltsev | Intel |  |  | alexander.maltsev@intel.com |
| Claudio da Silva | Intel |  |  | claudio.da.silva@intel.com |
| Carlos Cordeiro | Intel |  |  | carlos.cordeiro@intel.com |

Abstract

This document proposes specification text for subclause 30.5.6.4.4 of the spec describing block interleaver design for SC mode, [1].

**30.5.6.4.4 Block interleaver**

This subclause defines a block interleaver for 64QAM and 64NUC modulation. It performs modulated complex symbols interleaving inside the SC symbol block and its parameters depend on the *NSPB* and *NCB* parameters.

The input to interleaver scheme is a SC symbol block *din(q)* of length *NSBP* \* *NCB* composed of 64QAM (or 64NUC) symbols:

* , where *q* denotes SC symbol block number, *q* = 0, 1, …, *NBLKS*-1.

The output of interleaver scheme is a permuted SC symbol block of the same length defined as follows:

* , where *idx* defines the array of permutation indexes.

The array of permutation indexes *idx* is constructed as follows:

* , where *i* = 0, 1, …, (*NSPB*\**NCB*)/8-1 and *j* = 0, 1, …, 7.
* , where *i* = 0, 1, …, *Nx* -1 and *j* = 0, 1, …, *Ny* - 1.
* *Nx* = 4\**NCB*, *Ny* = (*NSPB*\**NCB*)/(*Nx*\*8).

**References:**

1. Draft P802.11ay\_D0.3