IEEE P802.11
Wireless LANs

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| PHY Miscellaneous Part-1 |
| Date: 2016-04-28 |
| Author(s): |
| Name | Affiliation | Address | Phone | email |
| Lochan Verma | Qualcomm Inc. | 5775 Morehouse Dr, San Diego, CA 92121 | +1-858-845-7832 | lverma@qti.qualcomm.com |
| Sameer Vermani | 5775 Morehouse Dr, San Diego, CA 92121 | +1-858-845-3115 | svverman@qti.qualcomm.com |
| Bin Tian |  |  |  |

Abstract

This submission removes TBDs from PHY Clause 26 in accordance with text already present in D0.4.

Changes suggested to **26.3.14.5.5** are already motioned in 11-16/866r4

Changes suggested to **26.3.19** are for the following reason.

Default value of Reserved bits in HE-SIG-A of HE PPDUs are set to 1. The Reserved HE-SIG-A Indication represents an exception when the Reserved bits in HE-SIG-A are set incorrectly, i.e., set to 0 in lieu of being set to 1.

Changes suggested to **Table 26-17** HE-SIG-A field of an HE trigger-based PPDU

Revisions:

* Rev 0: Initial version of the document.

Interpretation of a Motion to Adopt

A motion to approve this submission means that the editing instructions and any changed or added material are actioned in the TGax Draft. This introduction is not part of the adopted material.

***Editing instructions formatted like this are intended to be copied into the TGax Draft (i.e. they are instructions to the 802.11 editor on how to merge the text with the baseline documents).***

***TGax Editor: Editing instructions preceded by “TGax Editor” are instructions to the TGax editor to modify existing material in the TGax draft. As a result of adopting the changes, the TGax editor will execute the instructions rather than copy them to the TGax Draft.***

**26.3.14.5.5 Resource Allocation for an HE Trigger-based PPDU**

***TGax Editor: In accordance with 11-16/866r4 please change the line 9 on page 226 as follows:***

~~Details TBD.~~

**26.3.19 HE receive procedure**

***TGax Editor: Change the line 48 on page 239 as follows:***

Reserved HE-SIG-A Indication is defined as an HE-SIG-A with Reserved bits equal to 0 ~~or TBD.~~

***TGax Editor: Change the line 6 on page 240 as follows:***

Reserved HE-SIG-A Indication is defined as an HE-SIG-A with Reserved bits equal to 0 ~~or TBD.~~

***TGax Editor: Change the line 36 on page 239 as follows:***

Reserved HE-SIG-A Indication is defined as an HE-SIG-A with Reserved bits equal to 0 ~~or TBD.~~

**26.3.10.7.2 Content**

***TGax Editor: Change the line 22-25 on page 167 as follows:***

|  |  |  |  |
| --- | --- | --- | --- |
| B23 | Reserved | 1 | Reserved and set to ~~value indicated in the Trigger frame~~ 1.NOTE: Unlike other Reserved fields in HE-SIG-A of HE\_TRIG PPDU, B23 does not have a corresponding bit in the Trigger frame |

**9.3.1.23 Trigger frame format**

***TGax Editor: Change the line 22 on page 30 as follows:***

The HE-SIG-A Reserved subfield indicate the values of the reserved bits in the HE-SIG-A2 of the trigger-based PPDU transmitted as a response to the Trigger frame. ~~The number of reserved bits is 9.~~ Bits B54 to B62 in the Trigger frame are set to 1 and correspond to the bits B7 to B15 in the HE-SIG-A2 of the HE trigger-based PPDU with B54 in the Trigger frame corresponding to B7 in the HE-SIG-A2 of the HE trigger-based PPDU and so on.

**References:**

1. **IEEE P802.11axTM/D0.4, Aug 2016.**