IEEE P802.11  
Wireless LANs

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| **Comment Resolution for Subclause 8.3.4a.1.5** | | | | |
| **Date:** 2013-07-01 | | | | |
| **Author(s):** | | | | |
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Abstract

This document provides comment resolution for TGah Draft 0.1 Comment Collection 9 with these CIDs: 23, 275, 276, 277, and 278.

Interpretation of a Motion to Adopt

A motion to approve this submission means that the editing instructions and any changed or added material are actioned in the TGah Draft. This introduction is not part of the adopted material.

***Editing instructions formatted like this are intended to be copied into the TGah Draft (i.e. they are instructions to the 802.11 editor on how to merge the text with the baseline documents).***

***TGah Editor: Editing instructions preceded by “Instruction to Editor” are instructions to the TGah editor to modify existing material in the TGah draft. As a result of adopting the changes, the TGah editor will execute the instructions rather than copy them to the TGah Draft.***

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| **CID** | **P.L** | **SC** | **Comment** | **Proposed Change** | **Resolution** |
| 23 | 50.50 | 8.3.4a.1.5 | BA ID and Bitmap sizes are TBD in this section. | Set Bitmap size to 8 and 16 as described in 9.21.2 and rest of bits as BA ID (2 and 6). | Revised –  Tgah editor to make changes shown in 11-13-0818-01-00ah under the heading for CID 23. |
| 275 | 50.32 | 8.3.4a.1.5 | The size of the BlockAck ID in the NDP MAC frame body of NDP Block ACK (1MHz) is currently TBD. | The size of the BlockAck ID in the NDP MAC frame body of NDP Block ACK (1MHz) is 6 bits. The size of the BlockAckID field in Table 8-33l should be changed from "TBD" to "6". Change the TBD's in the description of the BlockAckID field in Table 8-33l to 6. | Revised –  See the proposed resolution for CID 23. |
| 276 | 50.42 | 8.3.4a.1.5 | The size of the BlockAck Bitmap field in the NDP MAC frame body of NDP Block ACK (1MHz) is currently TBD. | The size of the BlockAck Bitmap field in the NDP MAC frame body of NDP Block ACK (1MHz) is 4 bits. The size of the BlockAck Bitmap field in Table 8-33l should be changed from "TBD" to "4". Change the TBD's in the description of the BlockAck Bitmap field in Table 8-33l to 4. | Revised –  See the proposed resolution for CID 23. |
| 277 | 51.3 | 8.3.4a.1.5 | The size of the BlockAck ID in the NDP MAC frame body of NDP Block ACK (2MHz) is currently TBD. | The size of the BlockAck ID in the NDP MAC frame body of NDP Block ACK (2MHz) is 14 bits. The size of the BlockAckID field in Table 8-33m should be changed from "TBD" to "14". Change the TBD's in the description of the BlockAckID field in Table 8-33m to 14. | Revised –  See the proposed resolution for CID 23. |
| 278 | 51.14 | 8.3.4a.1.5 | The size of the BlockAck Bitmap field in the NDP MAC frame body of NDP Block ACK (2MHz) is currently TBD. | The size of the BlockAck Bitmap field in the NDP MAC frame body of NDP Block ACK (2MHz) is 8 bits. The size of the BlockAck Bitmap field in Table 8-33m should be changed from "TBD" to "8". Change the TBD's in the description of the BlockAck Bitmap field in Table 8-33m should be changed to 8. | Revised –  See the proposed resolution for CID 23. |

## Discussion:

*The commenters are correct that the values are TBD in subclause 8 for NDP BA. However, the value of the BlockAck Bitmap field for 1MHz NDP BA is already defined in D0.1 and set to 8 in subclause 9.21.2 (P136L1) and the value of the BlockAck Bitmap field for 2MHz NDP BA is already defined in D0.1 and set to 16 in subclause 9.21.2 (P136L2). Hence, the proposed comment resolution is to be inline with existing values of these fields in TGah D0.1.*

*The remaining bits are assigned to the BA ID field: 2 and 6 bits for 1MHz and 2MHz NDP BA, respectively*.

* **NDP Block ACK**

**Instruction to Editor: *Please make the following changes in clause 8.3.4a.1.5:***

NDP MAC frame body of NDP Block ACK frame contains the information listed in Table 8-33l (NDP MAC frame body of NDP Block ACK (1MHz)) and Table 8-33m (NDP MAC frame body of NDP Block ACK (2MHz)).

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| * **NDP MAC frame body of NDP Block ACK (1MHz)** | | |
| Field | Size (bits) | Description |
| NDP MAC  Frame Type | 3 | NDP MAC Frame Type field is set to 4. |
| BlockAck ID | 2 | The BlockAck ID field is 2 bits in length and contains the identifier of the NDP BlockAck frame. It is set to the 2 LSBs of the Scrambler (as defined in 20.3.11.3 (Scrambler)) of the PSDU that carries the soliciting A-MPDU or the BlockAckRequest. |
| Starting Sequence Control | 12 | The Starting Sequence Control field is 12 bits in length and contains the sequence number of the first MSDU or A-MSDU for which the NDP BlockAck frame is sent. The value of this field is defined in 9.21.7.5 (Generation and transmission of BlockAck by an HT STA). |
| BlockAck Bitmap | 8 | The Block Ack Bitmap field of the NDP BlockAck frame is 8 bits in length and is used to indicate the received status of up to 8 MSDUs and A-MSDUs. Each bit that is equal to 1 in the NDP Block Ack bitmap acknowledges the successful reception of a single MSDU or A-MSDU in the order of sequence number, with the first bit of the NDP Block Ack bitmap corresponding to the MSDU or A-MSDU with the sequence number that matches the value of the Starting Sequence Control field. |

The NDP MAC frame body of NDP Block ACK for >=2MHz has the structure defined in Table 8-33m (NDP MAC frame body of NDP Block ACK (2MHz)).

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| * **NDP MAC frame body of NDP Block ACK (2MHz)** | | |
| Field | Size (bits) | Description |
| NDP MAC  Frame Type | 3 | NDP MAC Frame Type field is set to 4. |
| BlockAck ID | 6 | The BlockAck ID field is 6 bits in length and contains the identifier of the NDP BlockAck frame. It is set to the 6 LSBs of the Scrambler (as defined in 20.3.11.3 (Scrambler)) of the PSDU that carries the soliciting A-MPDU or the BlockAckRequest. |
| Starting Sequence Control | 12 | The Starting Sequence Control field is 12 bits in length and contains the sequence number of the first MSDU or A-MSDU for which the NDP BlockAck frame is sent. The value of this field is defined in 9.21.7.5 (Generation and transmission of BlockAck by an HT STA). |
| BlockAck Bitmap | 16 | The Block Ack Bitmap field of the NDP BlockAck frame is 16 bits in length and is used to indicate the received status of up to 16 MSDUs and A-MSDUs. Each bit that is equal to 1 in the NDP Block Ack bitmap acknowledges the successful reception of a single MSDU or A-MSDU in the order of sequence number, with the first bit of the NDP Block Ack bitmap corresponding to the MSDU or A-MSDU with the sequence number that matches the value of the Starting Sequence Control field. |