

APRIL 1986

Orlando Section Notes



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IEEE



Dr. Brian Petrasko

The Engineer of the Year

CHAIRMAN'S CORNER

April 1986

As you have noticed while reading the Notes over the past few months, there has been some changes made to the format. Most of these were done to reduce the cost, however, a subcommittee of the executive board is considering other changes. We would like **ANY INPUT** from you, the readers, as to the things you like/dislike about the present publication or any new additions that is felt would improve the Notes. It would tickle us to death if we could get your ideas about the Section Notes on how to increase its value to engineers in the area. It only takes a few minutes to give myself or one of the executive committee your input. It would be nice to hear from more people outside of the executive committee than we have at this point. Those of you that have come to the monthly meeting or passed on information to us, it has been greatly appreciated.

The meeting place for the April executive meeting will be Automation Intelligence at 1200 West Colonial Drive at 5:30 p.m. If you wish to attend, feel free to do so because these meetings are open to the membership.

George Taack
Chairman
Orlando Section

The "Orlando Section Notes" is published monthly by the Orlando Section of the Institute of Electrical and Electronics Engineers, Inc. Copies are mailed automatically at no cost by the first of each month to some 1200 IEEE members in Central Florida. To be considered for publication, articles and meeting notices must be received by the Editor by the 15th of the month preceding publication. Advertising space is available to those organizations and individuals interested in publicizing their products, services, or identities to a specialized audience. Enquiries may be addressed to the Editor, Norm Lawton, P. O. Box 536, Casselberry, FL 32707, telephone 305-365-2713 (business).

APRIL/MAY 1986 CALENDAR

April

- 1st Executive Committee Meeting
- 3rd Computer Society — CMOS Digital Circuits — Fault Diagnosis Design for Testability and Built-in Testing
- 15th MTT/AP — Microstrip Antennas

May

- 6th Executive Committee Meeting
- 15th MTT/AP — TBA
- 20th Electron Devices — TBD
- TBD COM/VT — Spread Spectrum Communications

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ALL WEEK	\$775	\$950
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THURSDAY April 10 9 a.m.-5 p.m.	Knowledge-Based Systems Stephen F. Smith	Software Testing Edward Miller	Tools for Developers of Distributed Software Systems Jack C. Wileden
FRIDAY April 11 9 a.m.-5 p.m.	13 Prolog & Knowledge Information Processing Doug DeGroot	14 Knowledge-Based Systems for Software Development and Maintenance Allen Goldberg	15 Reliability and Performance of Distributed Database Systems Leszek Lilien

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Early Registration Fees (POSTMARKED by 3-21-86)

	MEMBER	NON-MEMBER
ALL WEEK	\$675	\$850
PER TUTORIAL	\$160	\$200

TRACK
1

TRACK
2

TRACK
3

	1	2	3
MONDAY April 7 9 a.m.-5 p.m.	1 <i>Ada™</i> Joseph E. Urban	2 Software Quality Assurance- A Practical Approach Tsun S. Chow	3 Introduction to Computer Networks & Distributed Operating Systems John A. Stankovic
TUESDAY April 8 9 a.m.-5 p.m.	4 The UNIX™ Operating System John Carson	5 Software Configuration Management I Stan Siegel Elizabeth Below	6 Concurrent and Distributed Programming Arthur Bemstein
WEDNESDAY April 9 9 a.m.-5 p.m.	7 The C Programming Language Alan Feuer	8 Software Configuration Management II Stanley Siegel Elizabeth Below	9 Fault-Tolerance in Distributed Real-Time Systems K.H. Kim

MTT AND AP

- TITLE:** MICROSTRIP ANTENNAS
- SPEAKER:** Mr. Robert Munson, Ball Aerospace
- DATE:** Tuesday, April 15, 1986
- TIME:** 5:30 p.m.
- PLACE:** Hewlett Packard
6177 Lake Ellenor Drive
- PROGRAM:** There will be an optional dinner at Gary's Duck Inn following the meeting. Guests are welcome.
- POINT OF CONTACT:** For any meeting information, contact Bob Smallwood (859-2900).

COMPUTER SOCIETY

- TITLE:** CMOS Digital Circuits — Fault Diagnosis Design for Testability and Built-in Testing
- ABSTRACT:** Complementary MOS (CMOS) technology is becoming an important, if not the dominant, VLSI Technology. Failures in CMOS digital circuits have to be handled in ways different than the ones proposed earlier for bipolar and other MOS technologies, thus requiring new fault diagnosis, testable designs and built-in testing of CMOS logic circuits will be presented.
- SPEAKER:** Dr. S. M. Reddy, Professor and Chairman
Department of Electrical and Computer Engineering
University of Iowa
- DATE:** Thursday, April 3, 1986
- TIME/PLACE:** University of Central Florida
Computer Science Bldg. — CC2 Room 103
3:00 p.m. - 4:30 p.m.
Kane Furniture Conference Room
2601 E. Colonial Drive, Orlando
7:00 p.m. - 9:00 p.m.
- ADMISSION:** None. All members, students and industrial representatives are invited.
- POINT OF CONTACT:** For any more information, contact Jorge Medina (834-9000) or Steve Forsythe (244-1166).

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The Executive Committee meets the first Tuesday of each month, except July, at AUTOMATION INTELLIGENCE, INC., 1200 W. COLONIAL DRIVE, (corner of 441 and Colonial), at 5:30 p.m. All IEEE members are welcome to attend.

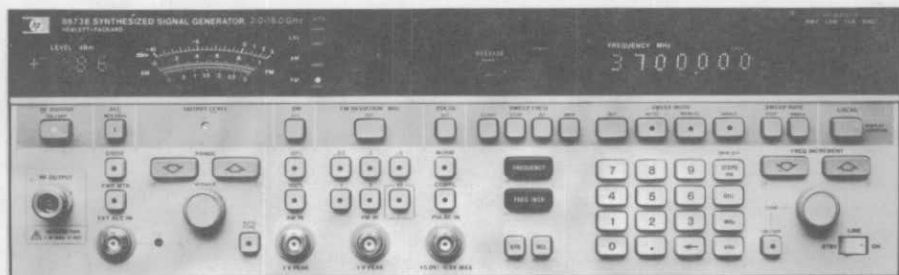
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