CDAC Vega processor 2 Day workshop, Celebration of India Council day by IEEE Kolkata Section

IEEE KOLKATA SECTION

IEEE INDIA COUNCIL

VEGA PROCESSOR

IEEE KOLKATA SECTION PRESENTS

Hands On Workshop on

VEGA

PROCESSOR ECOSYSTEM

India's Indigenous Microprocessor developed by C-DAC

Date: 17 and 18 April 2023

Venue: Electrical Engineering Department, Jadavpur University

ABOUT VEGA MICROPROCESSOR

Digital India RISC-V Microprocessor (DIR-V) Program was launched by Ministry of Electronics and Information Technology (MeitY) as one of the concrete steps towards "Atmanirbhar Bharat", with an aim to enable creation of Microprocessors for the future, in India, for the world and achieve industry-grade silicon. This program will make India not only a RISC-V Talent Hub for the World but also supplier of RISC-V SoCs for a wide range of applications. Centre for Development of Advanced Computing (C-DAC) a premier R&D organization under the MeitY, Govt. of India for carrying out Research and Development activities in IT, Electronics and associated areas for strengthening national technological capabilities. As part of the DIR-V Program C-DAC has successfully developed the VEGA series of microprocessors and related IPs and the complete ecosystem to enable fully indigenous product development that meets various requirements in the strategic, industrial and commercial sectors. These high-performance processors are based on the free and open-source RISC-V Instruction Set Architecture.

ABOUT WORKSHOP

This is a national workshop on the newly launched indigenous RISC-V based VEGA Processors. Experts from C-DAC, who are part of VEGA Processor development team, will be the resource persons. The DIR-V VEGA workshop will provide an insight into the VEGA series of processors and ecosystem focusing on both the theoretical and practical aspects of the VEGA processors, development boards, SDK and on building applications. The hands-on sessions will be conducted on the ARIES development board which is Arduino compatible. Certificates will be issued after the successful completion of the workshop.

WORKSHOP CONTENT

Day1:

- Introduction to VEGA Processors
- Interfacing sensors and modules
- Interfacing sensors and modules
- Application development using sensors and modules.

Day2:

- Introduction to IoT and interfacing IoT modules
- Introduction to Adafruit API and Alexa integration
- 'IoT Application development using modules and sensors

RESOURCE PERSONS

Shri. Libin T T
Scientist F
C-DAC Trivandrum, India

Shri. Premjith A. V.
Project Manager
C-DAC Trivandrum, India

Shri. Diwane Himanshu Kishor,
Project Engineer,
C-DAC Trivandrum, India

Register through the following Link :

Last Date of Registration : 09.04.23 (Maximum 60 candidates will be selected on the basis of first come first serve)

Fees for Non-IEEE members: Rs 500
Fees for IEEE members: Rs 200.

Shortlisted candidates must pay their Registration Fees between 10 -14 April, 2023 though GPay Ph. No. 9874377727